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## » Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

☐ 1. An asynchronous distributed approach for the simulation of behavior-level parallel processors

Ghosh, S.; Meng-Lin Yu;

Parallel and Distributed Systems, IEEE Transactions on

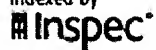
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## Key

IEEE JNL IEEE Journal or Magazine

IEEE JNL IEEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEEE CNF IEEE Conference Proceeding

IEEE STD IEEE Standard

1. Parallel logic simulation on a network of workstations using PVM

Kornicki, M.; Mahmood, A.; Carlson, B.S.;

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3. Study of a multilevel approach to partitioning for parallel logic simulation

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Sem, S.; Gowen, L.; Payne, M.; Sylvester, D.;

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8. Distributed logic simulation algorithm using preemption of inconsistent e

Raghu, C.S.; Sundaram, S.;

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9. Distributed logic simulation: time-first evaluation vs. event driven algorit

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11. Parallel logic simulation using Time Warp on shared-memory multiproce

Kim, H.K.; Chung, S.M.;

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- ☐ 16. Efficient parallel logic simulation techniques for the Connection Machine  
Chung, M.J.; Chung, Y.;  
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Jun, Y.-H.; Hall, I.N.; Lee, S.-H.; Park, S.-B.;  
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- ☐ 19. A parallel algorithm for logic simulation on transputer networks  
Srinivas, S.; Basu, A.; Paulraj, A.; Patnaik, L.M.;  
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